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APPLICANT(S): HSU et al.

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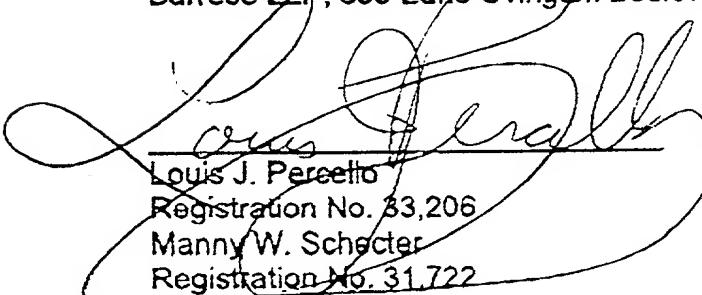
FOR: **SEMICONDUCTOR MEMORY SYSTEM HAVING DYNAMICALLY
DELAYED TIMING FOR HIGH-SPEED DATA TRANSFERS**

ASSOCIATE POWER OF ATTORNEY

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Dated: 8/27/01


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